1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 15 A
- Reverse voltage: V_R ≤ 50 V
- Extremely low forward voltage
- · High power capability due to clip-bonding technology and heat sink
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; $T_{sp} \le$ tbd °C; square wave	-	-	15	Α
V _R	reverse voltage	T _j = 25 °C	-	-	50	V
V _F	forward voltage	I_F = 15 A; $t_p \le 300 \mu s$; δ ≤ 0.02; T_j = 25 °C; pulsed	-	441	500	mV
I _R	reverse current	$V_R = 10 \text{ V; } t_p \le 3 \text{ ms; } \delta = 0.3;$ $T_j = 25 ^{\circ}\text{C; pulsed}$	-	30	70	μΑ
		$V_R = 50 \text{ V; } t_p \le 3 \text{ ms; } \delta = 0.3;$ $T_j = 25 \text{ °C; pulsed}$	-	480	1000	μΑ





5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Α	anode		K A
2	Α	anode	3	aaa-009063
3	К	cathode	2 CFP15 (SOT1289)	

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG050V150EPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG050V150EPD	050V 150E

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	50	V
IF	forward current	T _{sp} = tbd °C; δ = 1		-	21	Α
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{sp} ≤ tbd °C; square wave		-	15	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	780	mW
			<u>[2]</u>	-	1080	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

PMEG050V150EPD

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1][2]	-	-	160	K/W
			[1][3]	-	-	115	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	4	K/W

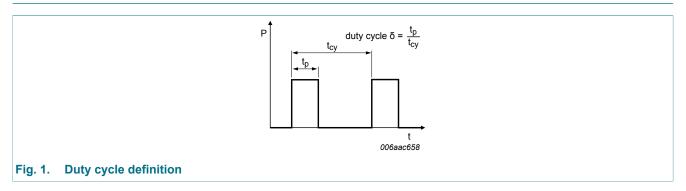
- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Soldering point of cathode tab.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{F}	forward voltage	I_F = 1 A; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C; pulsed	-	303	350	mV
		I_F = 5 A; t_p ≤ 300 μs; δ ≤ 0.02; T_j = 25 °C; pulsed	-	364	420	mV
		I_F = 10 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C; pulsed	-	406	-	mV
		I_F = 15 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C; pulsed	-	441	500	mV
I _R	reverse current	V_R = 5 V; $t_p \le 3$ ms; δ = 0.3; T_j = 25 °C; pulsed	-	20	-	μΑ
		V_R = 10 V; $t_p \le 3$ ms; δ = 0.3; T_j = 25 °C; pulsed	-	30	70	μΑ
		V_R = 30 V; $t_p \le 3$ ms; \overline{o} = 0.3; T_j = 25 °C; pulsed	-	92	-	μΑ
		$V_R = 50 \text{ V; } t_p \le 3 \text{ ms; } \delta = 0.3;$ $T_j = 25 \text{ °C; pulsed}$	-	480	1000	μΑ
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	1800	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	600	-	pF
$V_{(BR)R}$	reverse breakdown voltage	$I_R = 5 \text{ mA}; T_j = 25 \text{ °C}; t_p \le 1.2 \text{ ms};$ $\delta = 0.12; \text{ pulsed}$	50	-	-	V

11. Test information

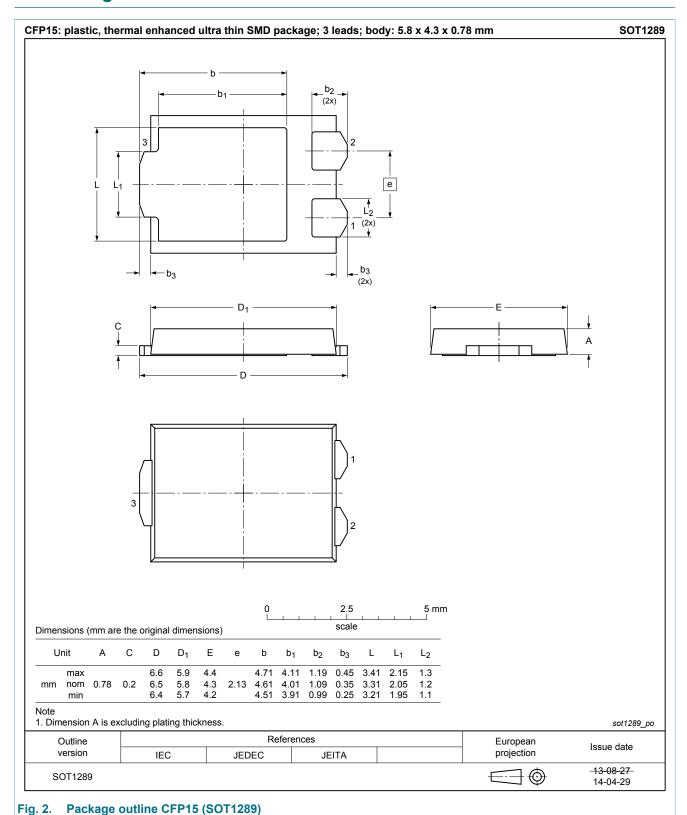


The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

11.1 Quality information

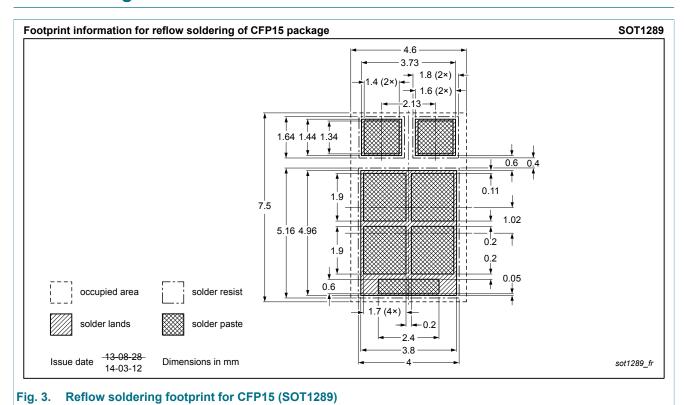
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



5/10

13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG050V150EPD v.2	20140704	Preliminary data sheet	-	PMEG050V150EPD v.1
Modifications:	Product status of	hanged		
PMEG050V150EPD v.1	20140519	Objective data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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